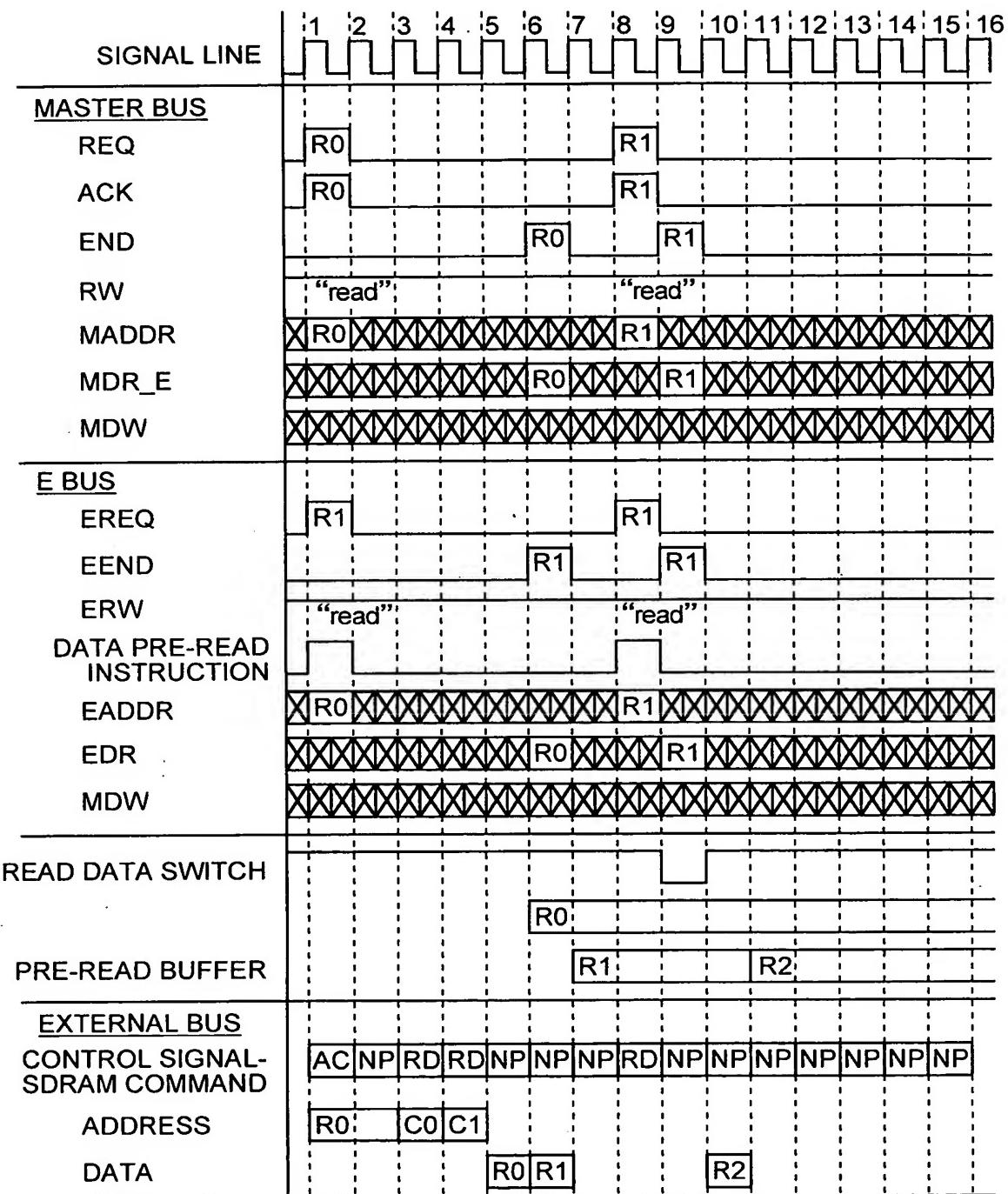
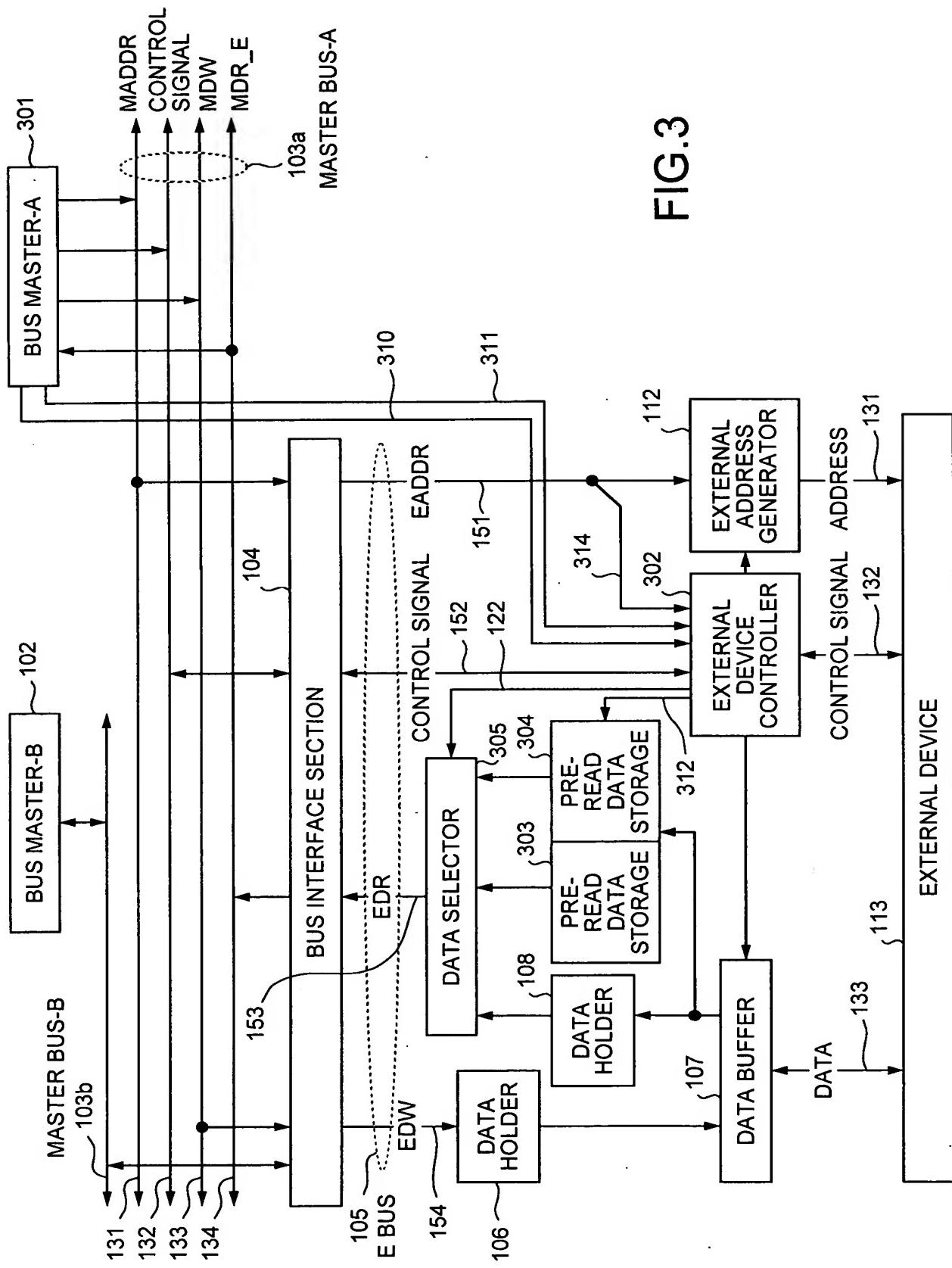


FIG.2



AC: LINE ACTIVATION COMMAND
 NP: NOP COMMAND
 RD: READ COMMAND



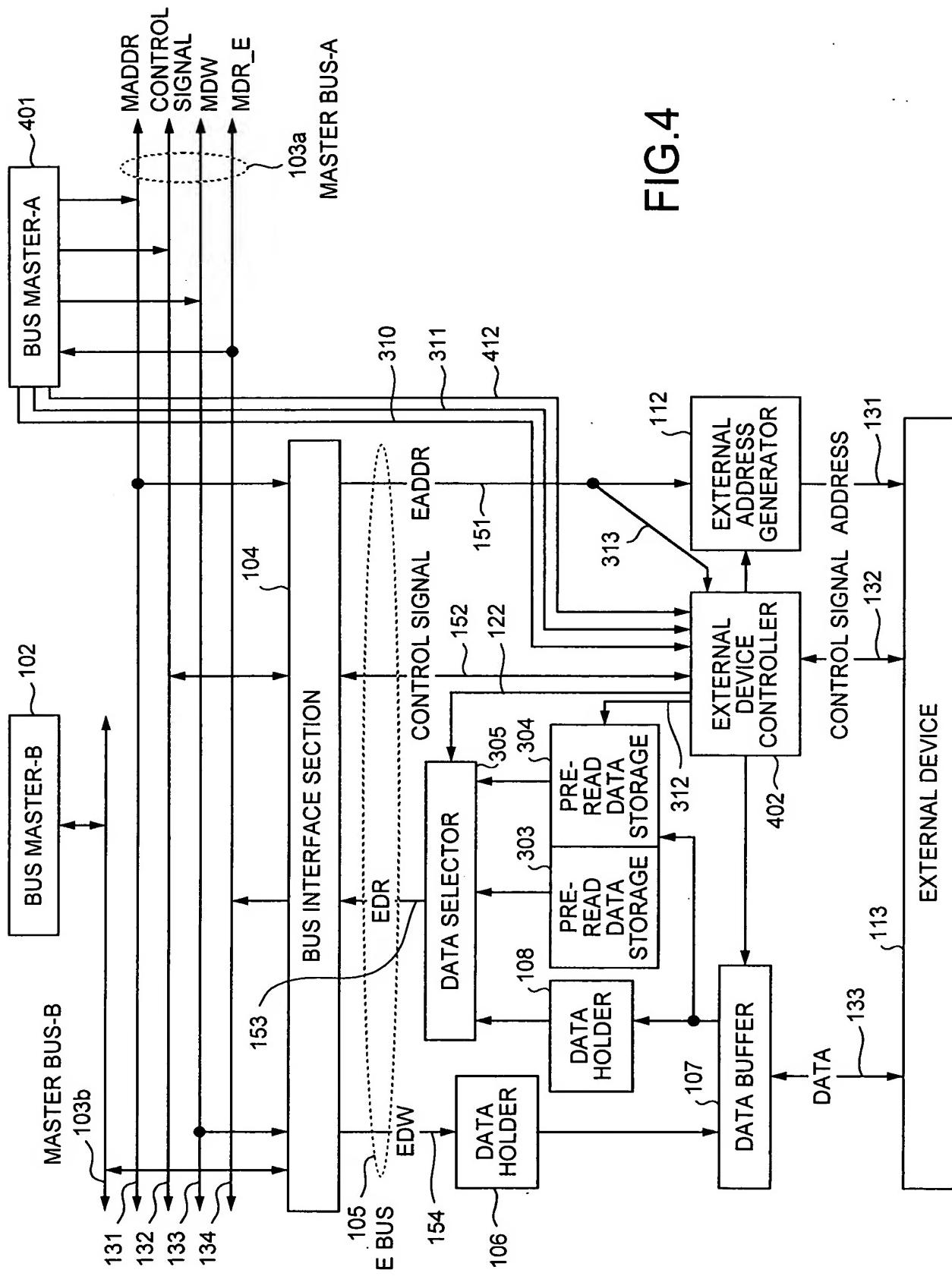
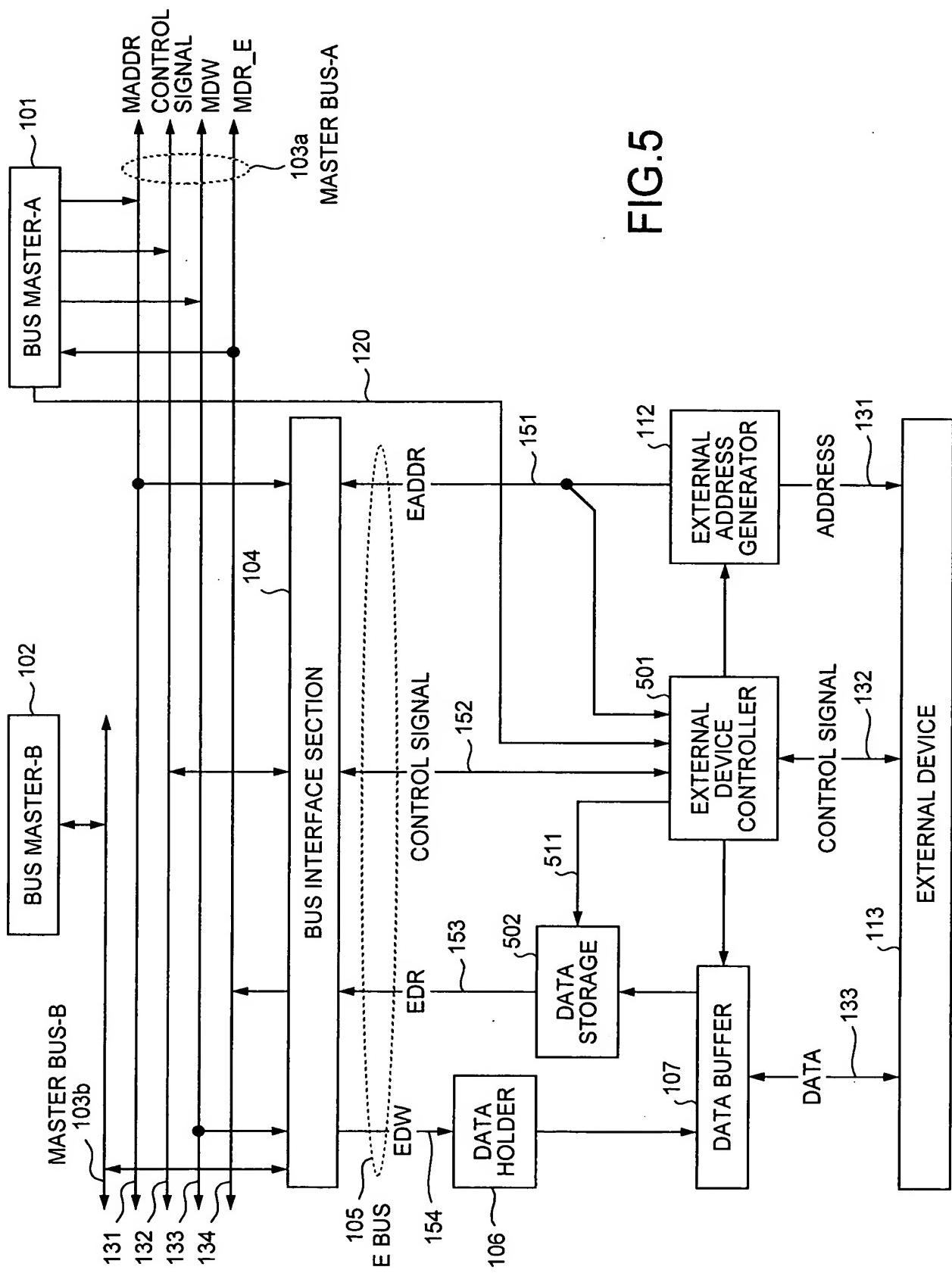


FIG.4



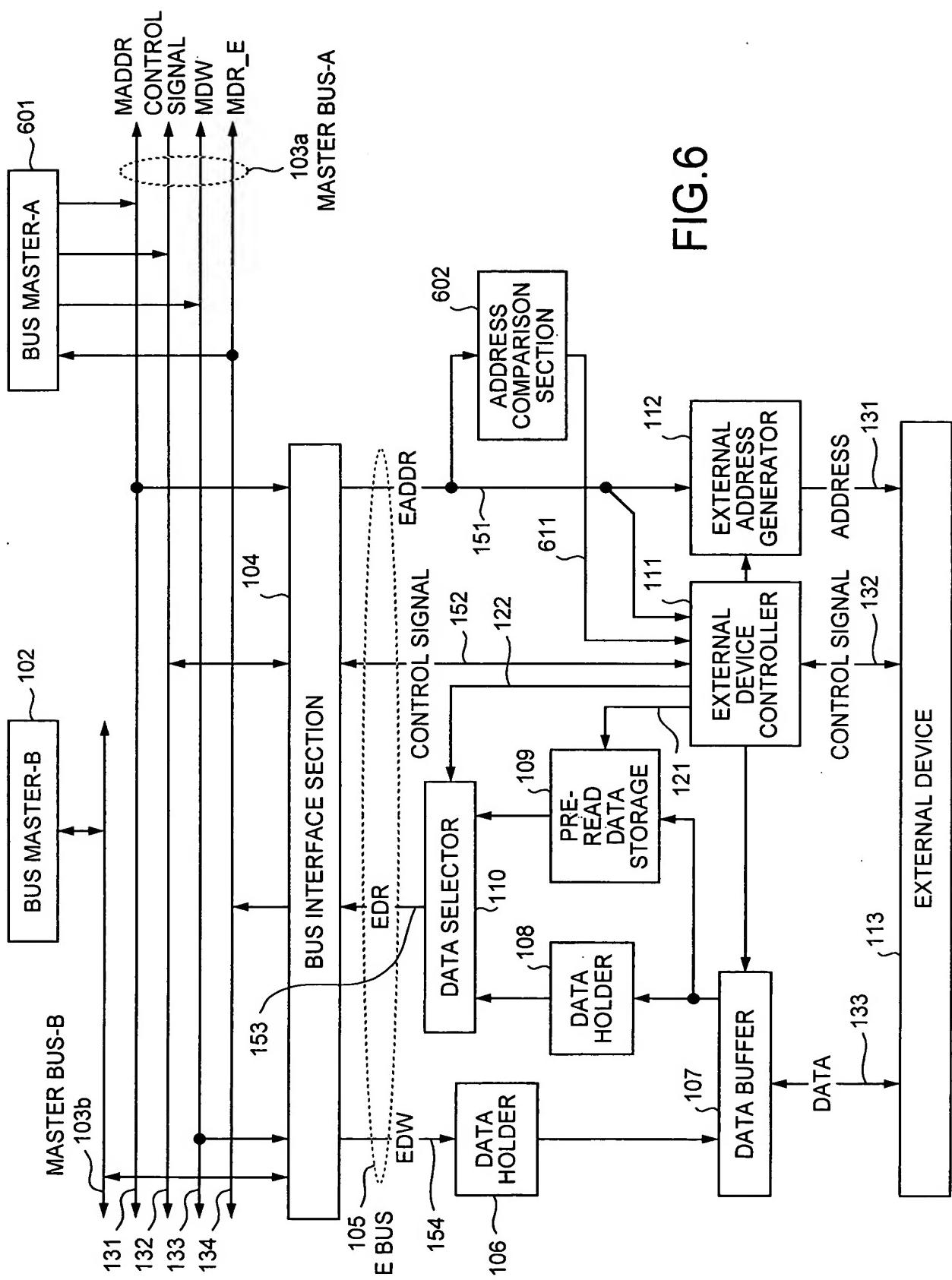
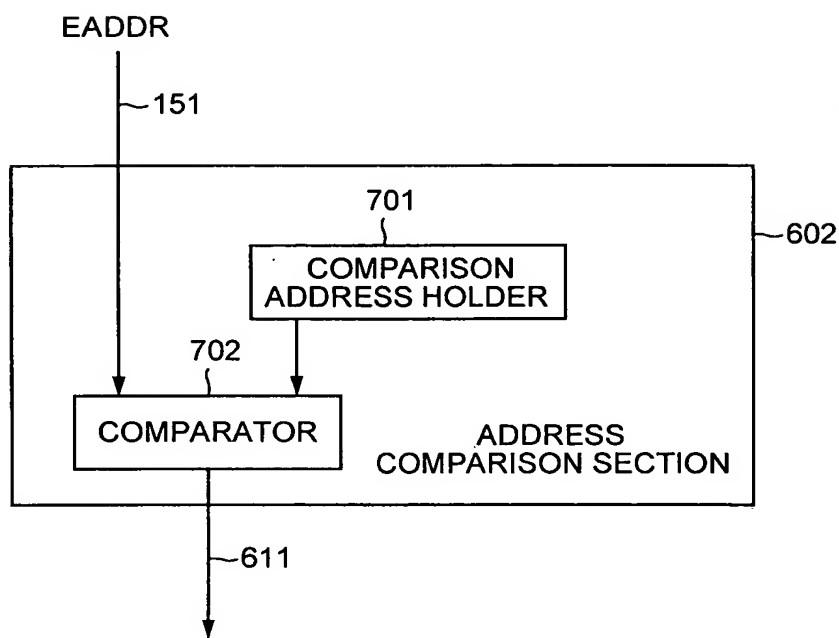


FIG.7



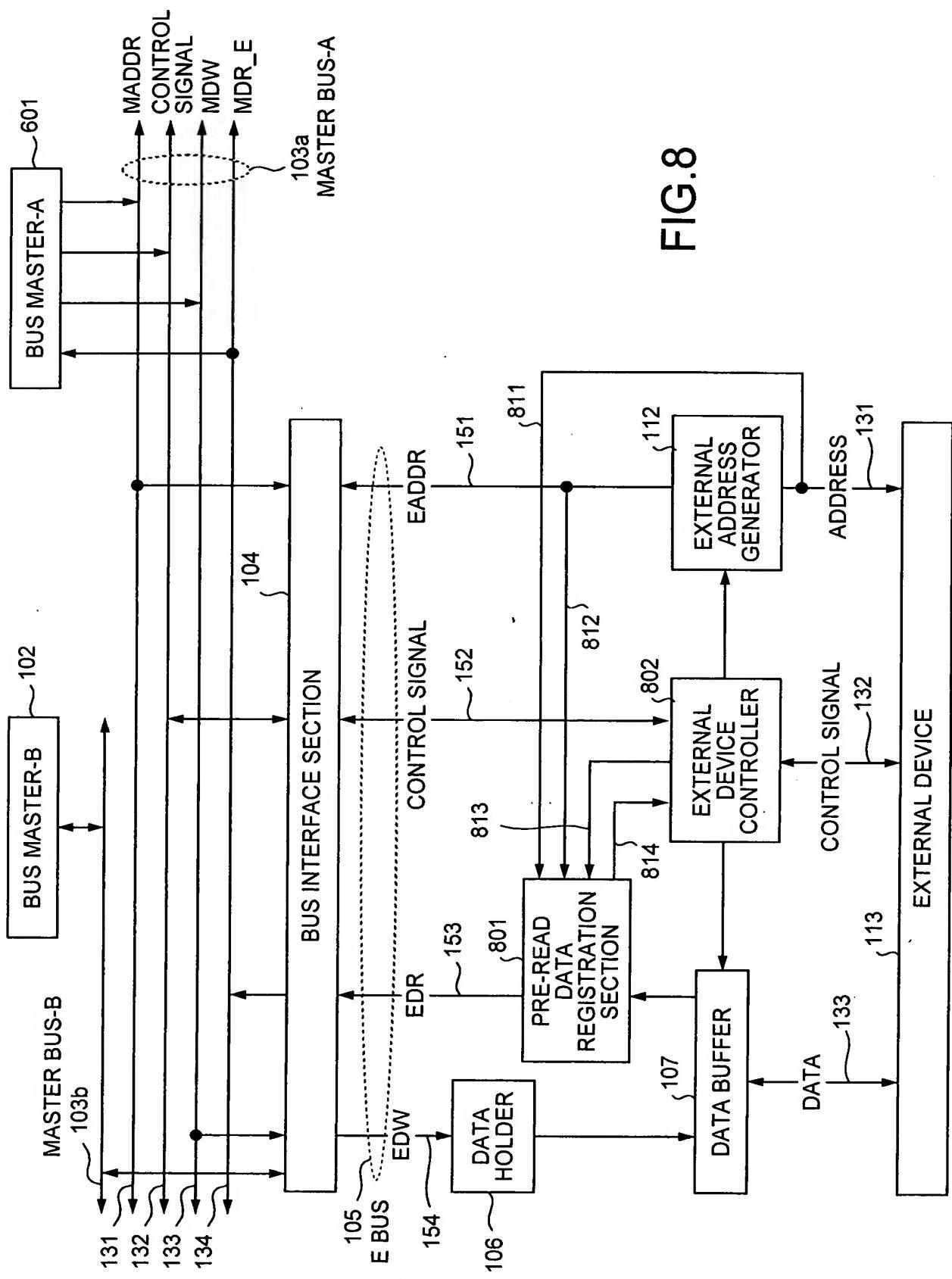


FIG.9A

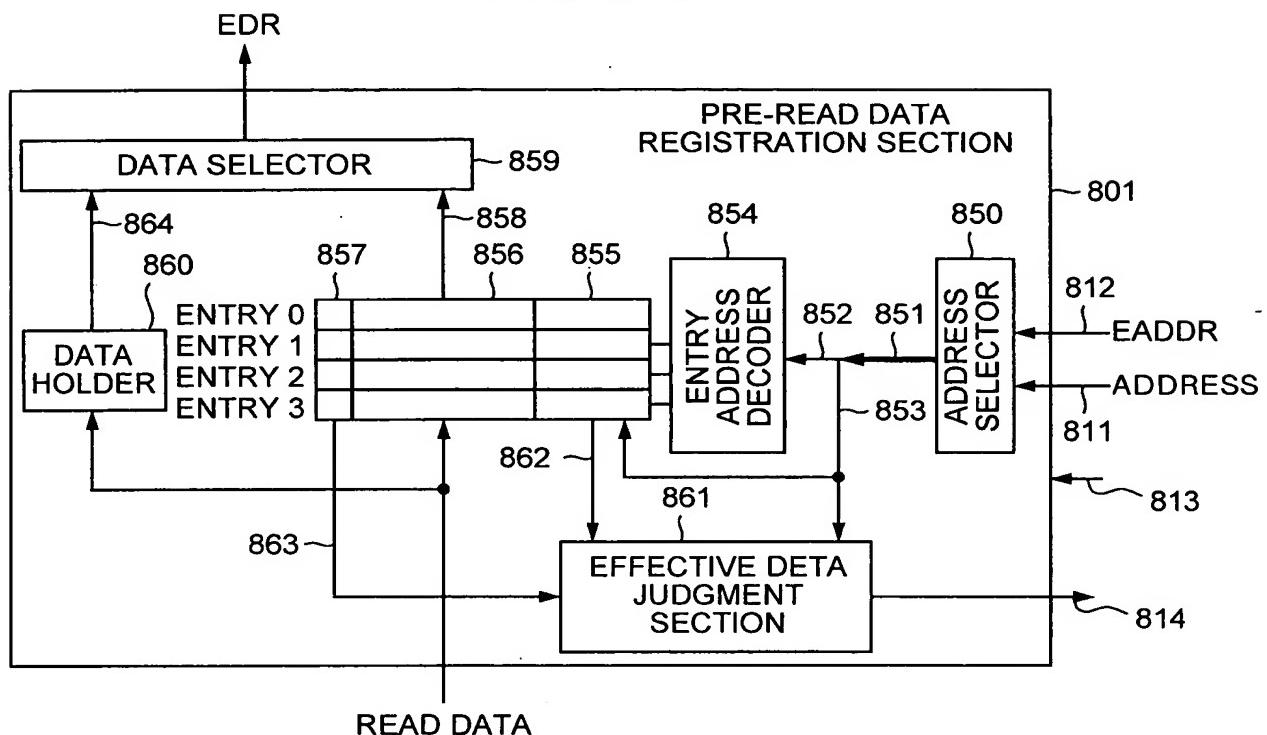
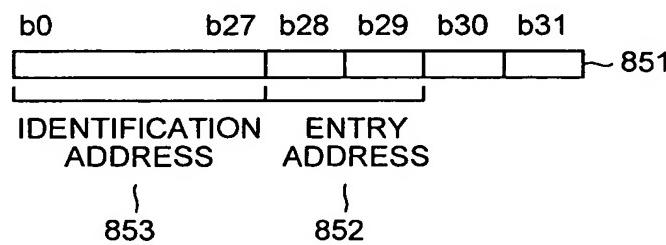


FIG.9B



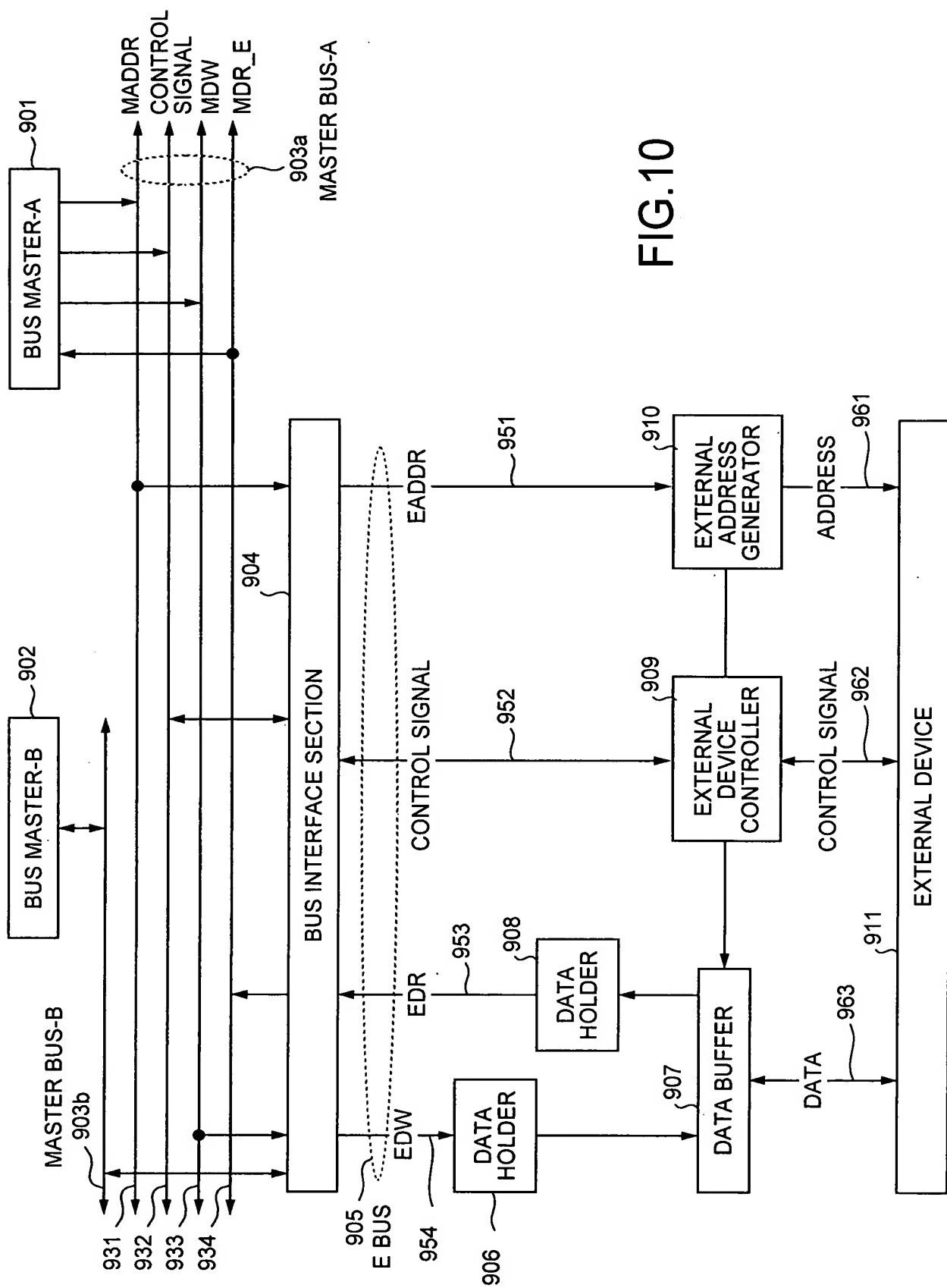


FIG.11

SIGNAL LINE	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
<hr/>																
<u>MASTER BUS</u>																
REQ	R0							R1								
ACK	R0							R1								
END							R0						R1			
RW																
MADDR	X	R0	X	X	X	X	X	X	R1	X	X	X	X	X	X	X
MDR_E	X	X	X	X	X	X	X	X	R0	X	X	R1	X	X	X	X
MDW	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
<u>E BUS</u>																
EREQ	R1							R1								
EEND								R1					R1			
ERW																
EADDR	X	R0	X	X	X	X	X	X	R1	X	X	X	X	X	X	X
EDR	X	X	X	X	X	X	X	X	R0	X	X	X	R1	X	X	X
EDW	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
<u>EXTERNAL BUS</u>																
CONTROL SIGNAL-SDRAM COMMAND	AC	NP	RD	NP	PR				AC	NP	RD	NP	PR			
ADDRESS	R0	C0							R1	C1						
DATA				R0									R1			

AC: LINE ACTIVATION COMMAND

NP: NOP COMMAND

RD: READ COMMAND

PR: BANK DEACTIVATION COMMAND